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8

Error-Correcting Codes

31 March 2022

Sebastian Wild

Learning Outcomes

1. Understand the context of *error-prone communication*.
2. Understand concepts of *error-detecting codes* and *error-correcting codes*.
3. Know and understand the *terminology of block codes*.
4. Know and understand *Hamming codes*, in particular 4+3 Hamming code.
5. Reason about the *suitability of a code* for an application.

Unit 8: *Error-Correcting Codes*



Outline

8 Error-Correcting Codes

- 8.1 Introduction
- 8.2 Lower Bounds
- 8.3 Hamming Codes

8.1 Introduction

Noisy Communication

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to decode a message in the presence of noise & errors?

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↪ We can

1. **detect errors** “This sentence has aao pi dgsdho gioasghds.”
2. **correct (some) errors** “Tiny errs ar corrected automaticly.”
(sometimes too eagerly as in the Chinese Whispers / Telephone)



Noisy Channels

- ▶ computers: copper cables & electromagnetic interference
 - ▶ transmit a binary string
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- ▶ We can aim at
 1. **error detection** ↪ can request a re-transmit
 2. **error correction** ↪ avoid re-transmit for common types of errors
 - ▶ This will require *redundancy*: sending *more* bits than plain message
 - ↪ **goal**: robust code with lowest redundancy
- ↪ that's the opposite of compression!

Clicker Question



What do you think, how many extra bits do we need to **detect** a **single bit error** in a message of 100 bits?

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Clicker Question



What do you think, how many extra bits do we need to **correct** a **single bit error** in a message of 100 bits?

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8.2 Lower Bounds

Block codes

► model:

- want to send message $S \in \{0, 1\}^*$ (bitstream) across a (*communication*) channel
 - any bit transmitted through the channel might *flip* ($0 \rightarrow 1$ resp. $1 \rightarrow 0$)
no other errors occur (no bits lost, duplicated, inserted, etc.)
 - instead of S , we send *encoded bitstream* $C \in \{0, 1\}^*$
sender *encodes* S to C , receiver *decodes* C to S (hopefully)
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▶ between 0 and n bits might be flipped

invalid code

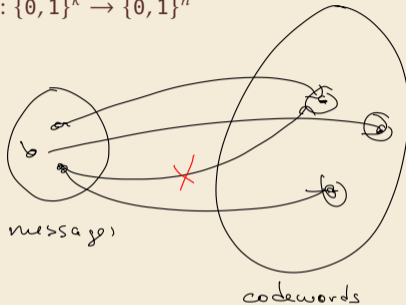
- ▶ how many flipped bits can we definitely **detect**?
- ▶ how many flipped bits can we **correct** without retransmit?

i. e. decoding m still possible

Code distance

$$m \neq m' \implies C(m) \neq C(m')$$

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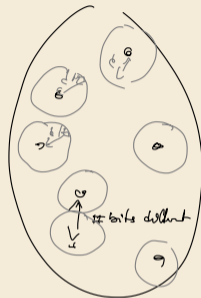
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$$d = \text{minimal Hamming distance of any two codewords} = \min_{x, y \in \mathcal{C}} d_H(x, y)$$



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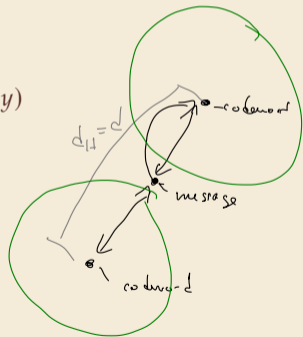
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Implications for codes

1. Need distance d to **detect** all errors flipping up to $d - 1$ bits.
2. Need distance d to **correct** all errors flipping up to $\lfloor \frac{d-1}{2} \rfloor$ bits.



Lower Bounds

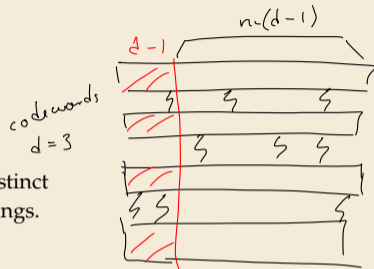
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- ▶ *proof sketch:* We have 2^k codeswords with distance d
after deleting the first $d - 1$ bits, all are still distinct
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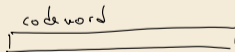
- ▶ **Hamming bound:** $2^k \leq \frac{2^n}{\sum_{f=0}^{\lfloor (d-1)/2 \rfloor} \binom{n}{f}}$

You know k, d

Guess n

\rightarrow bound tells if (potentially) possible

- ▶ *proof idea:* consider "balls" of bitstrings around codewords
count bitstrings with Hamming-distance $\leq t = \lfloor (d-1)/2 \rfloor$
correcting t errors means all these balls are disjoint
so $2^k \cdot \text{ball size} \leq 2^n$



$d=0$ 1 bitstring (codeword?)

$d=1$ n " (flip 1 bit)

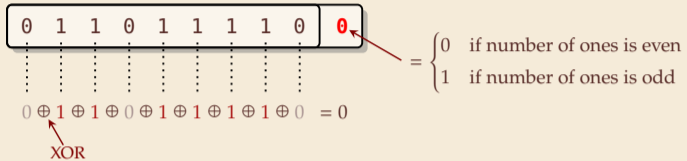
$d=f$ $\binom{n}{f}$

\rightsquigarrow We will come back to these.

8.3 Hamming Codes

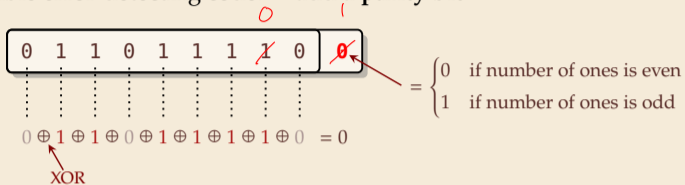
Parity Bit

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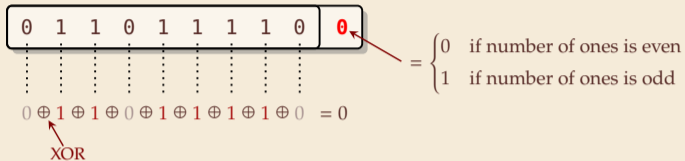


↪ code distance 2

- ▶ can detect any single-bit error (actually, any odd number of flipped bits)
- ▶ used in many hardware (communication) protocols
 - ▶ PCI buses, serial buses
 - ▶ caches
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👍 very simple and cheap

👎 cannot correct any errors

Clicker Question



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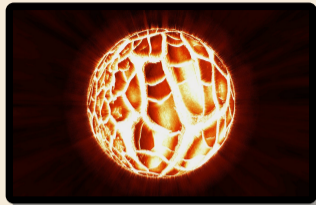
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Error-correcting codes

any downtime is expensive!

- ▶ typical application: heavy-duty server RAM
 - ▶ bits can randomly flip (e. g., by cosmic rays)
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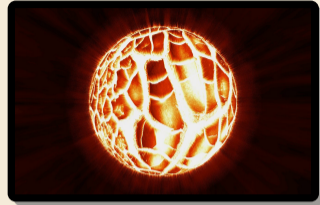


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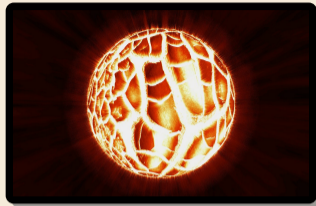
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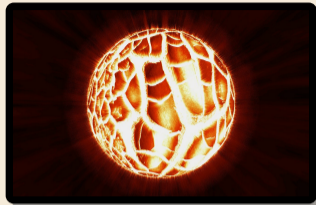
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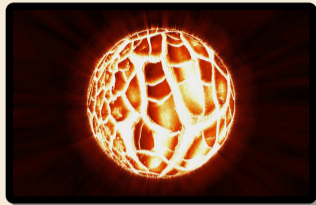
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instead of 200% (!)

Can do it with 11% extra memory!

How to locate errors?

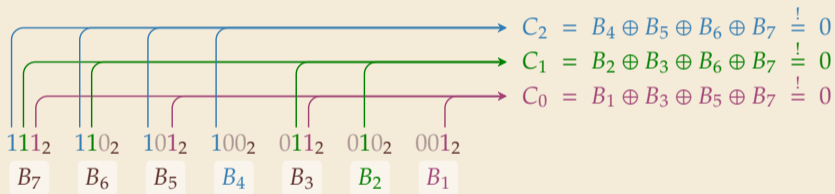
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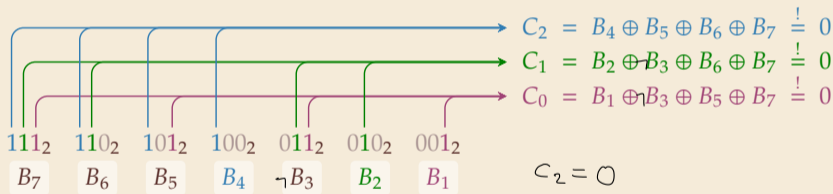
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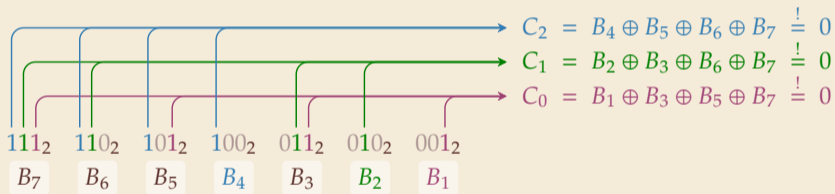
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Observe:

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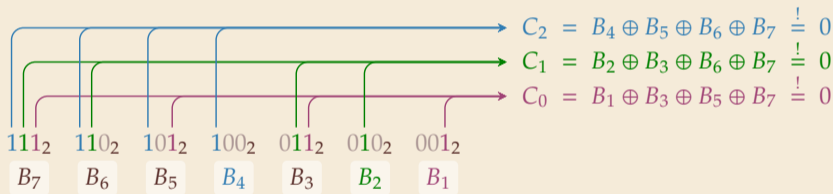
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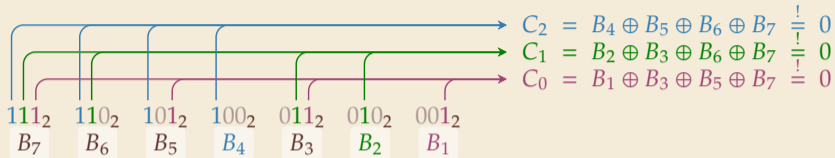
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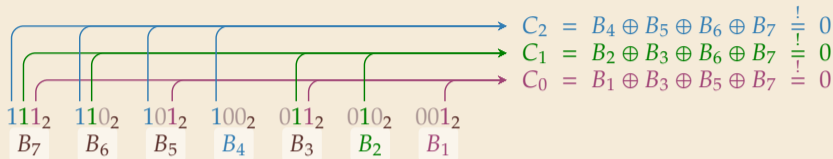
4+3 Hamming Code

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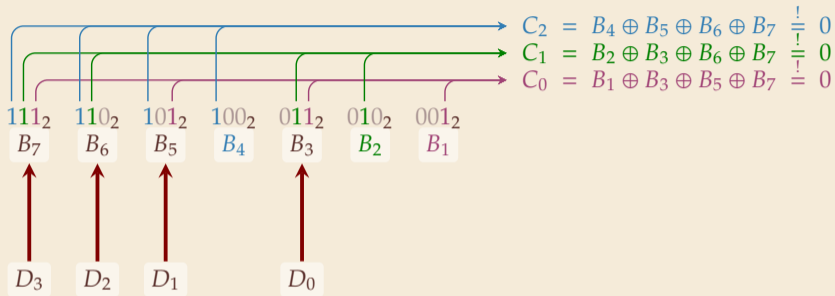
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- ▶ **4 + 3 Hamming Code – Encoding**

1. **Given:** message $D_3D_2D_1D_0$ of length $k = 4$

4+3 Hamming Code

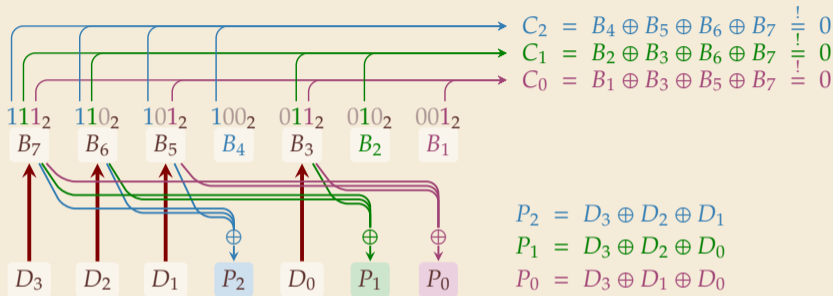
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 1. **Given:** message $D_3D_2D_1D_0$ of length $k = 4$
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4+3 Hamming Code

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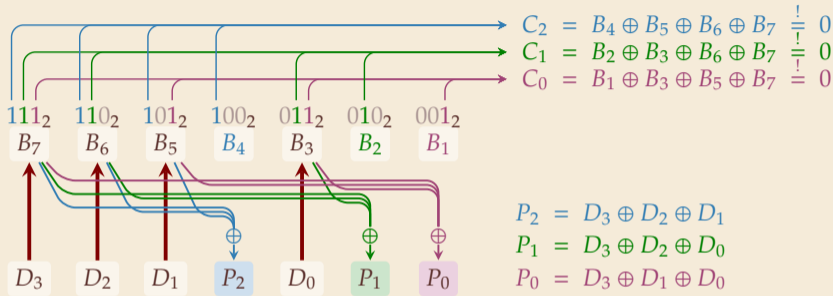
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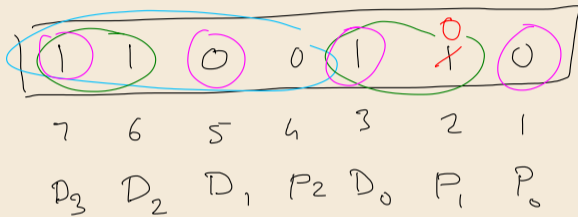


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4. send $D_3D_2D_1P_2D_0P_1P_0$

Example: message = 1101



$$C_2 = 0$$

$$C_1 = 1 \quad \rightarrow \text{bit 2 flipped}$$

$$C_0 = 0$$

4+3 Hamming Code – Decoding

► 4 + 3 *Hamming Code* – Decoding

1. **Given:** block $B_7B_6B_5B_4B_3B_2B_1$ of length $n = 7$
2. compute C (as above)
3. if $C = 0$ no (detectable) error occurred
otherwise, flip B_C (the C th bit was twisted)
4. return 4-bit message $B_7B_6B_5B_3$

Clicker Question



What is the code distance of 4 + 3 Hamming code?

3

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4+3 Hamming Code – Properties

- ▶ **Hamming bound:**

- ▶ 2^4 valid 7-bit codewords (on per message)

- ▶ any of the 7 single-bit errors corrected towards valid codeword

↪ each codeword covers 8 of all possible 7-bit strings

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- ▶ can *correct* any 1-bit error

- ▶ How about 2-bit errors?

- ▶ We can *detect* that *something* went wrong.
- ▶ **But:** above decoder mistakes it for a (different!) 1-bit error and “corrects” that
- ▶ Variant: store one additional parity bit for entire block
- ↪ Can *detect* any 2-bit error, but *not correct* it.

Hamming Codes – General recipe


- ▶ construction can be generalized:
 - ▶ Start with $n = 2^\ell - 1$ bits for $\ell \in \mathbb{N}$ (we had $\ell = 3$)
 - ▶ use the ℓ bits whose index is a power of 2 as parity bits
 - ▶ the other $n - \ell$ are data bits


Hamming Codes – General recipe

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 simple and efficient coding / decoding

 fairly space-efficient

Outlook

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i. e., one of 1, 4, 11, 26, 57, 120, 247, 502, 1013, . . .
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- ▶ For other scenarios, finding good codes is an active research area
 - ▶ information theory predicts that *almost all* randomly chosen codes are good(!)
 - ▶ but these are inefficient to decode

↪ clever tricks and constructions needed